	AMENDMENT TRANSMITTAL LETTER					M4065.0363/P363
OIPE	Application No. 09/653.541		Filing Date August 31, 2000		Examiner Art Unit Tu Tu V. Ho 2818	
AUG 0 5 2002	Au6 0 5 7002 Applicant(s): Mark E. Tuttle					
ير ا	Invention: MAGN	ETIC SHIELDI	NG FOR INTE	GRATED CIF	RCUITS	
	TO THE COMMISSIONER FOR PATENTS  Transmitted herewith is an amendment in the above-identified application.  The fee has been calculated and is transmitted as shown below.  CLAIMS AS AMENDED					
		Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
	Total Claims		. =		x	0.00
	Independent Claims		- =		. x	0.00
	Multiple Depend	le)				
	Other fee (please specify):  TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:  X Large Entity Small Entity					
						0.00
						,
	X No additional fee is required for this amendment.   Please charge Deposit Account No in the amount of \$   A duplicate copy of this sheet is enclosed.   A check in the amount of \$ to cover the filing fee is enclosed.					
	Payment by credit card. Form PTO-2038 is attached.					
	The Commissioner is hereby authorized to charge and credit Deposit Account No04-1073 as described below. A duplicate copy of this sheet is enclosed.					
	x Credit any overpayment.					
	x Charge any additional filling or application processing fees required under 37 CFR 1.16 and 1.17.					
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Docket No.: M4065.0363/P363 (PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Mark E. Tuttle

Application No.: 09/653,541

Filed: August 31, 2000

For: MAGNETIC SHIELDING FOR INTEGRATED CIRCUITS

Group Art Unit: 2818

Examiner: Tu Tu V. Ho

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## AMENDMENT

## **BOX:** Non-Fee Amendment

Commissioner for Patents Washington, DC 20231

Dear Sir:

In response to the Office Action dated May 3, 2002, rejecting claims 1-40 and 64-86, please amend the above-captioned U.S. Patent application as follows:

## In the Claims

Please replace claims 32 and 70 with amended claims 32 and 70.

32. (amended) An integrated circuit chip containing structures which may be affected by external magnetic fields, said chip comprising a magnetic field shielding material in contact with a surface of said chip, wherein said surface is a back surface of the chip.

70. (amended) A method of packaging a semiconductor device comprising:

electrically coupling a die carrier to a first surface of a die, said first surface being opposite to a second surface of said die; and